

100

FIGURE 1A

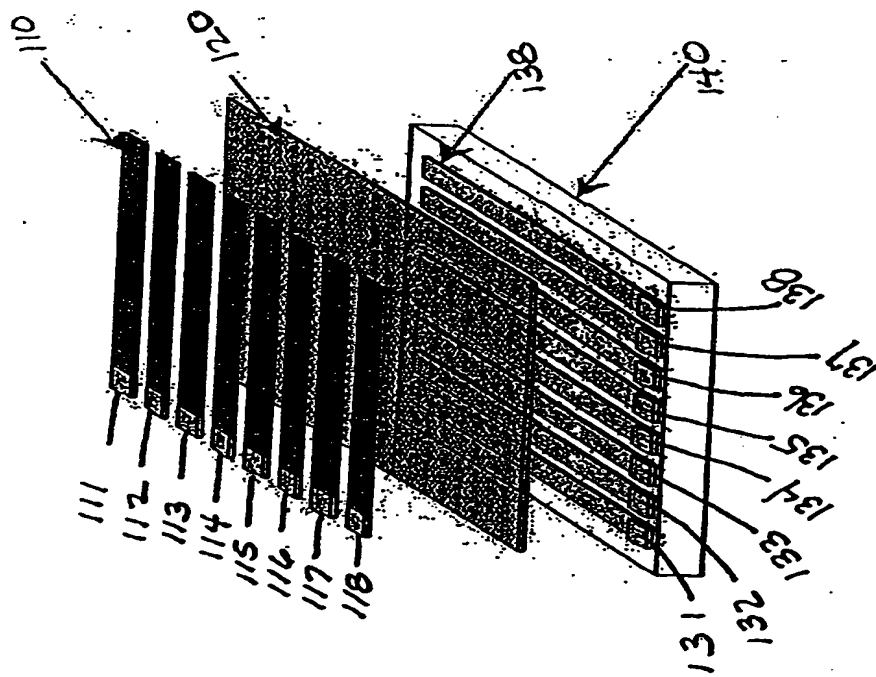
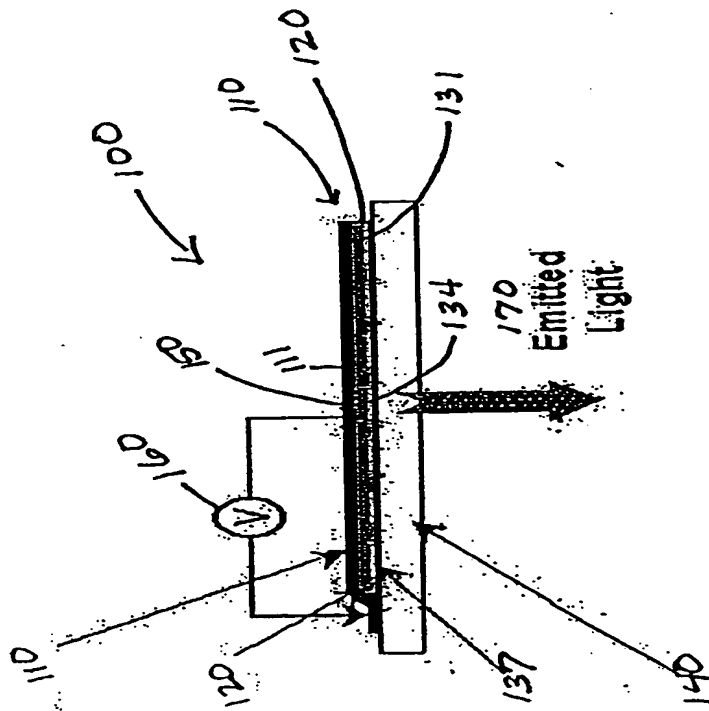
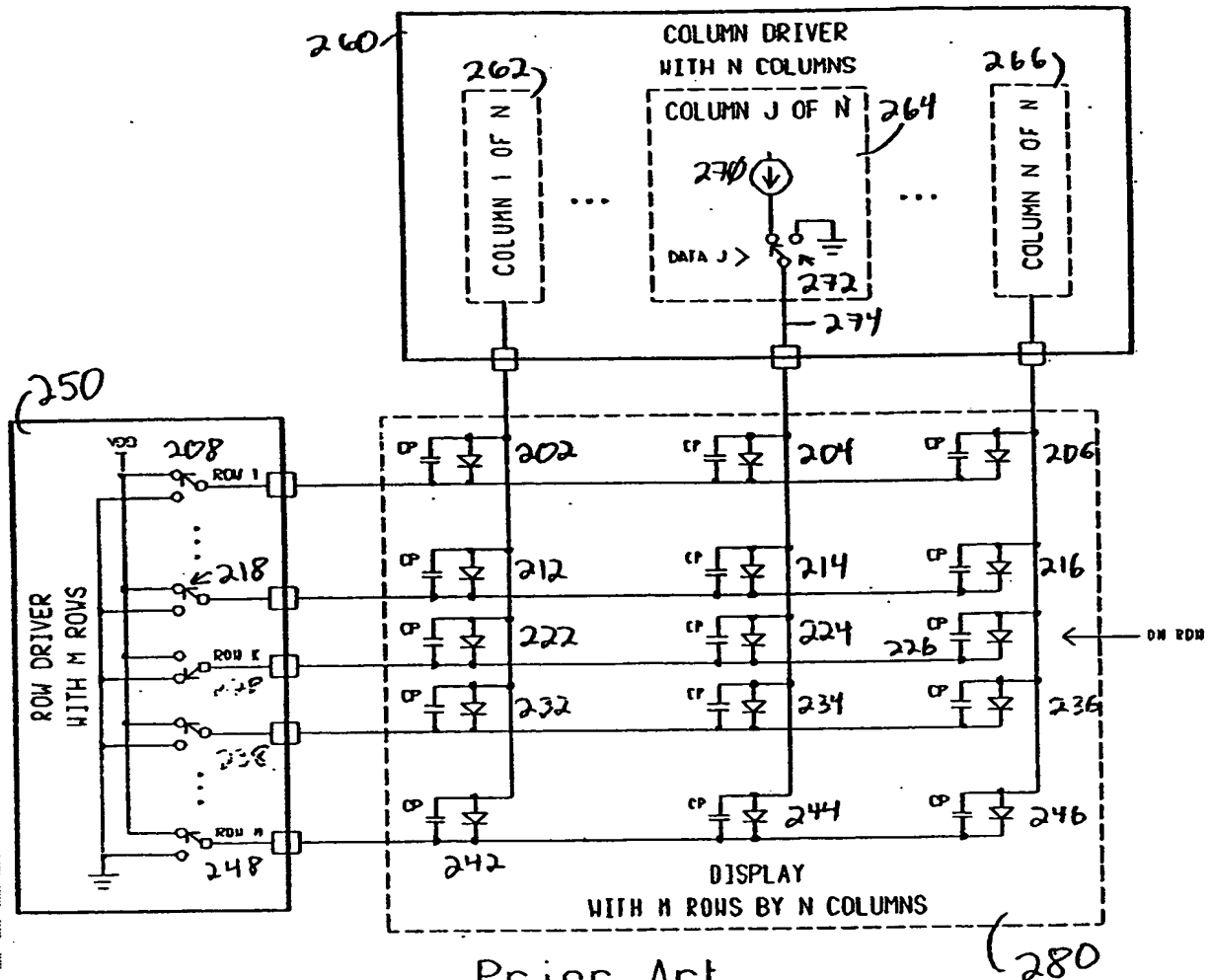


FIGURE 1B





Prior Art
Figure 2

FIG. 3 is a block diagram of a digital image sensor system 300. The system includes a digital block 340, a column driver block 300, and a channel output block 308. The digital block 340 contains three lookup tables: TABLE 1 (NDTV LOOKUP TABLE 364), TABLE 2 (PIXEL OFFSET COMPENSATION LOOKUP TABLE 348), and TABLE 3 (COLUMN RESISTANCE CORRECTION LOOKUP TABLE 352). It also includes a digital averaging block 356. The column driver block 300 includes a channel select block 304, a D/A converter 328, and a calibration circuit 354. The channel output block 308 provides the final channel output 308.

FIG. 3

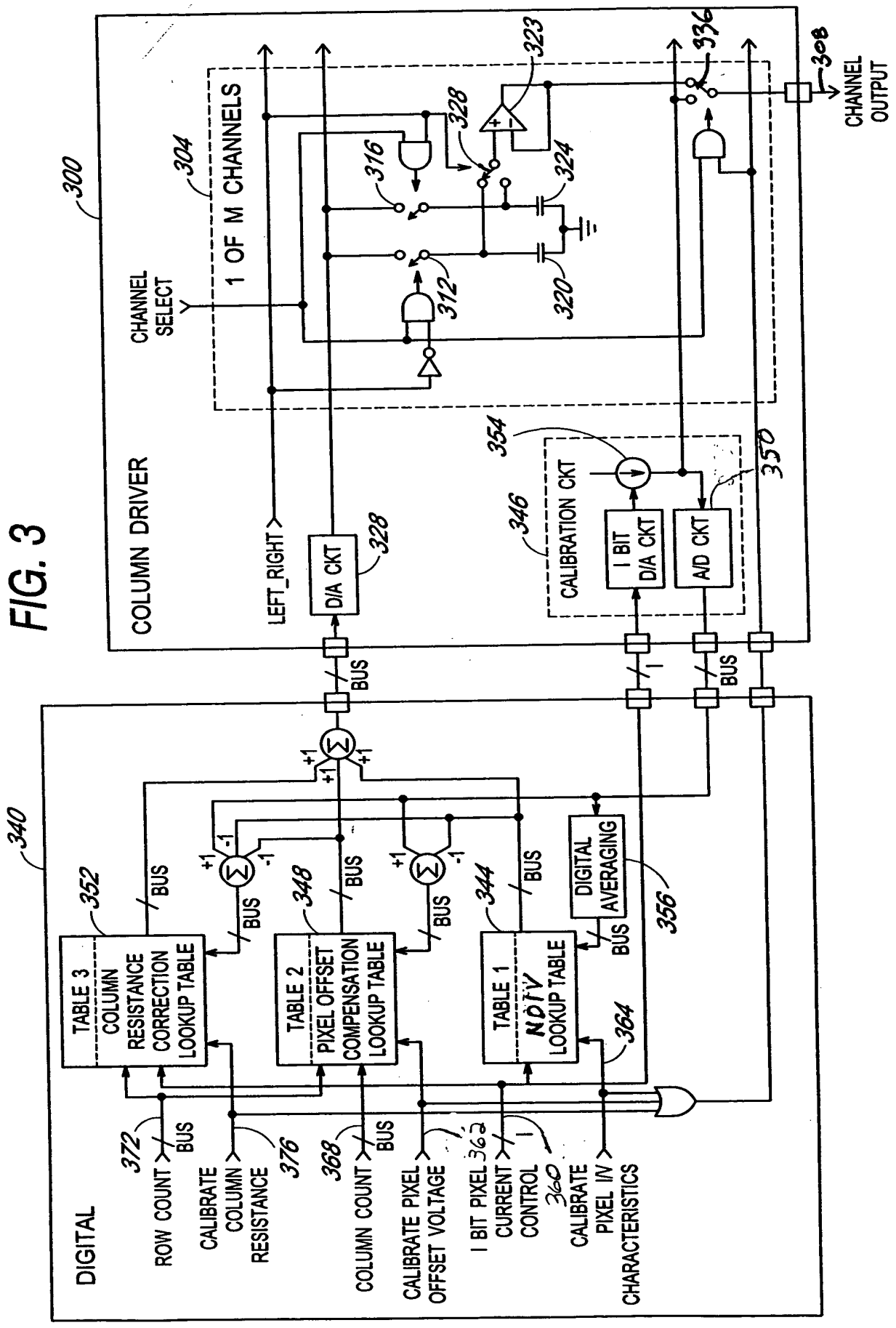
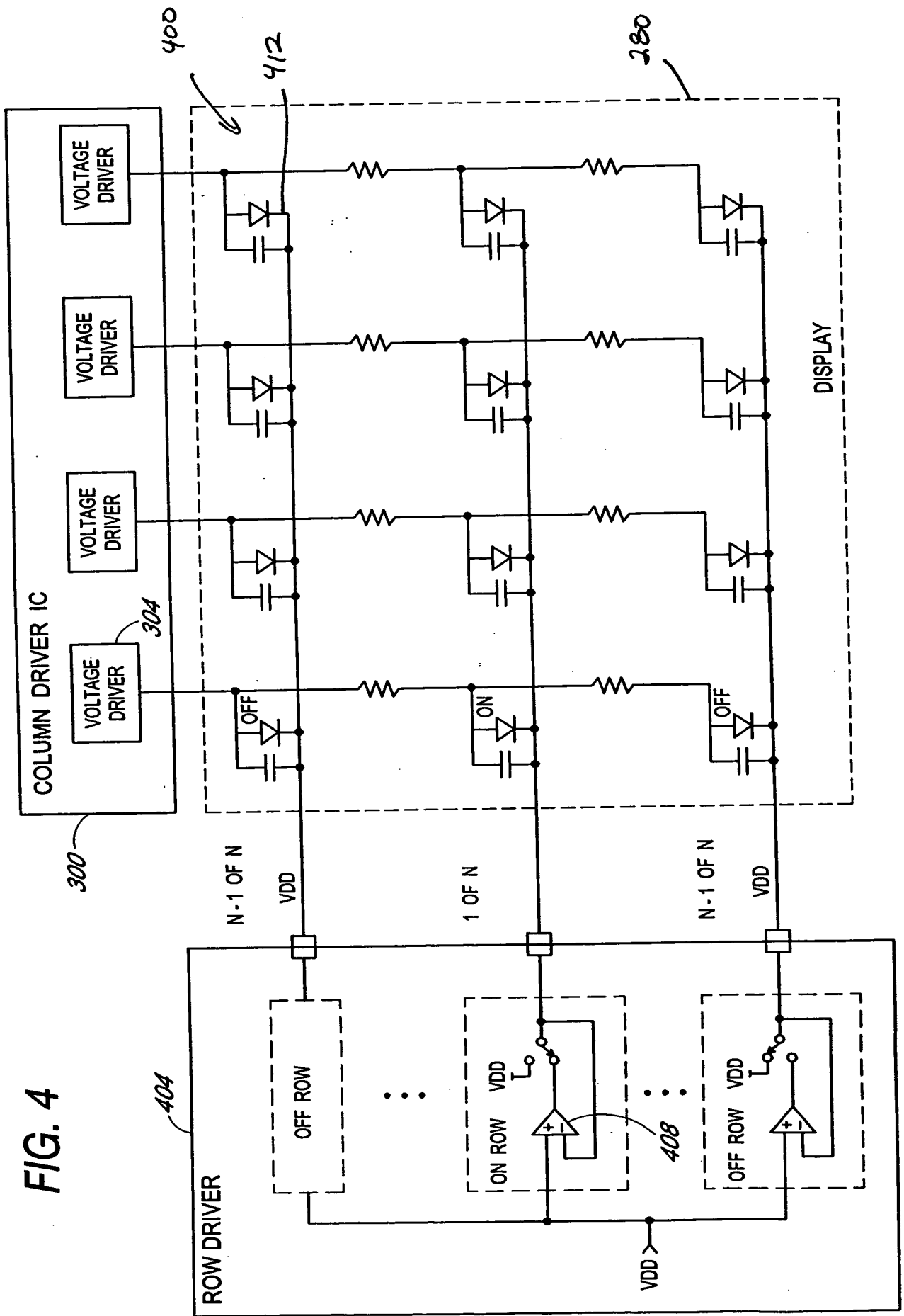


FIG. 4



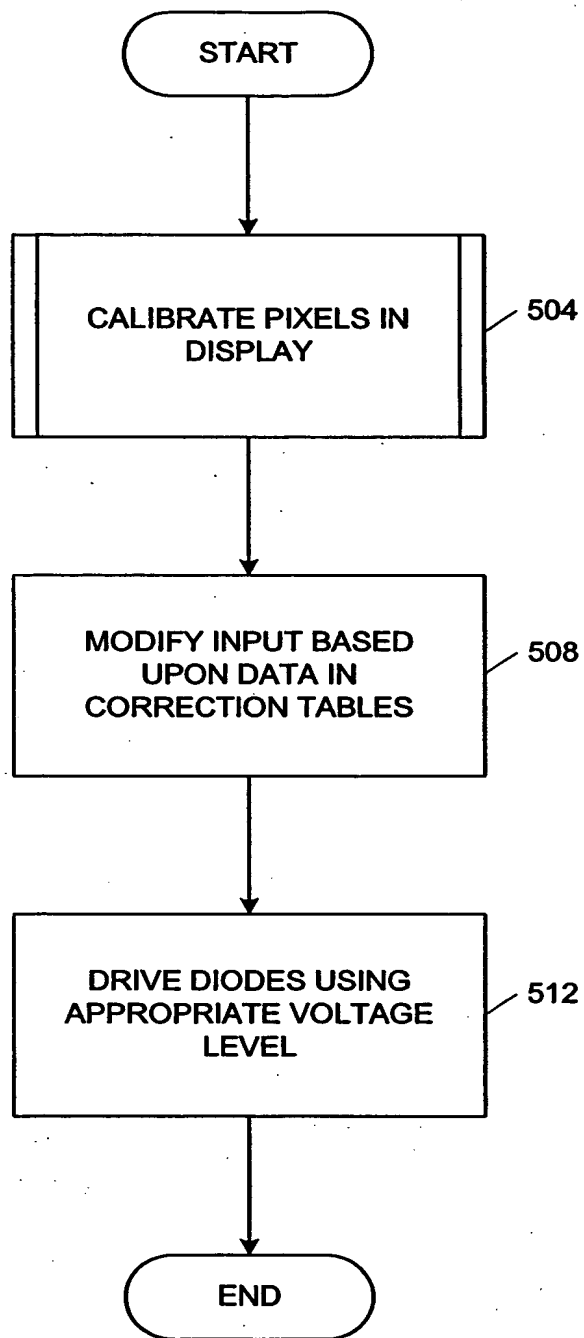


FIG. 5

504

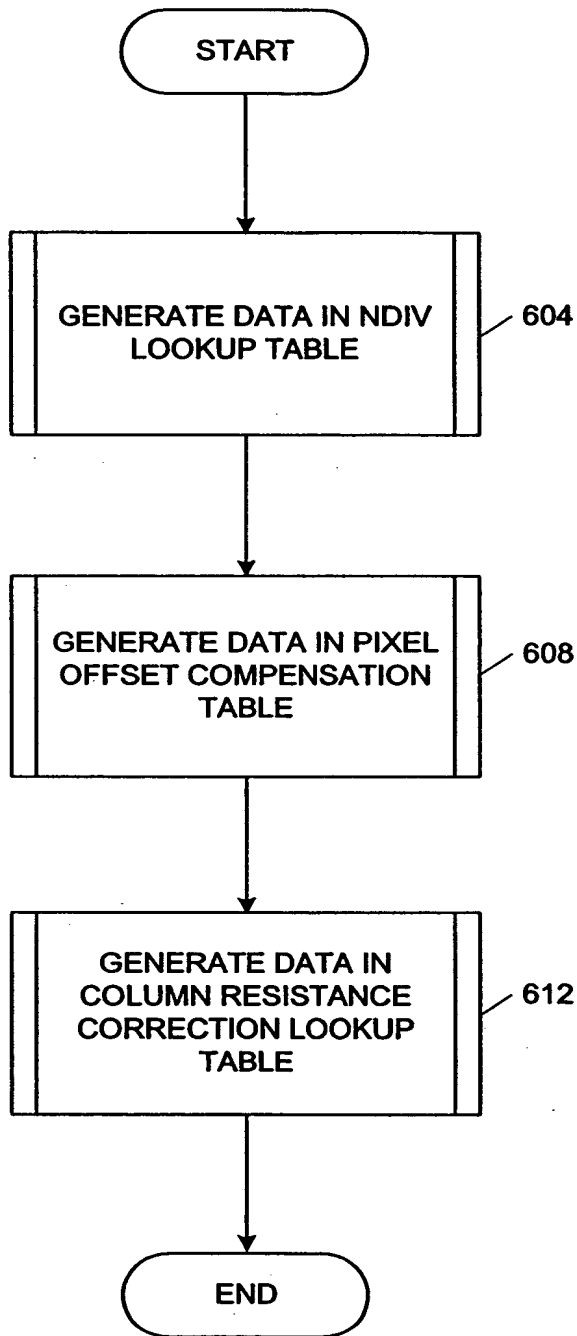


FIG. 6

604

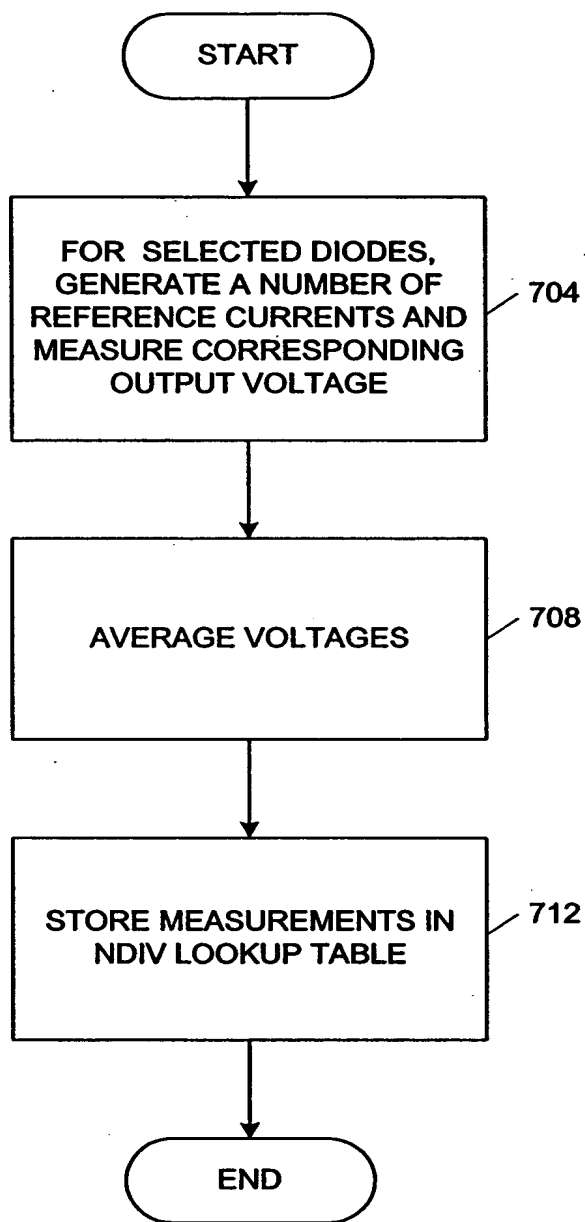


FIG. 7

608

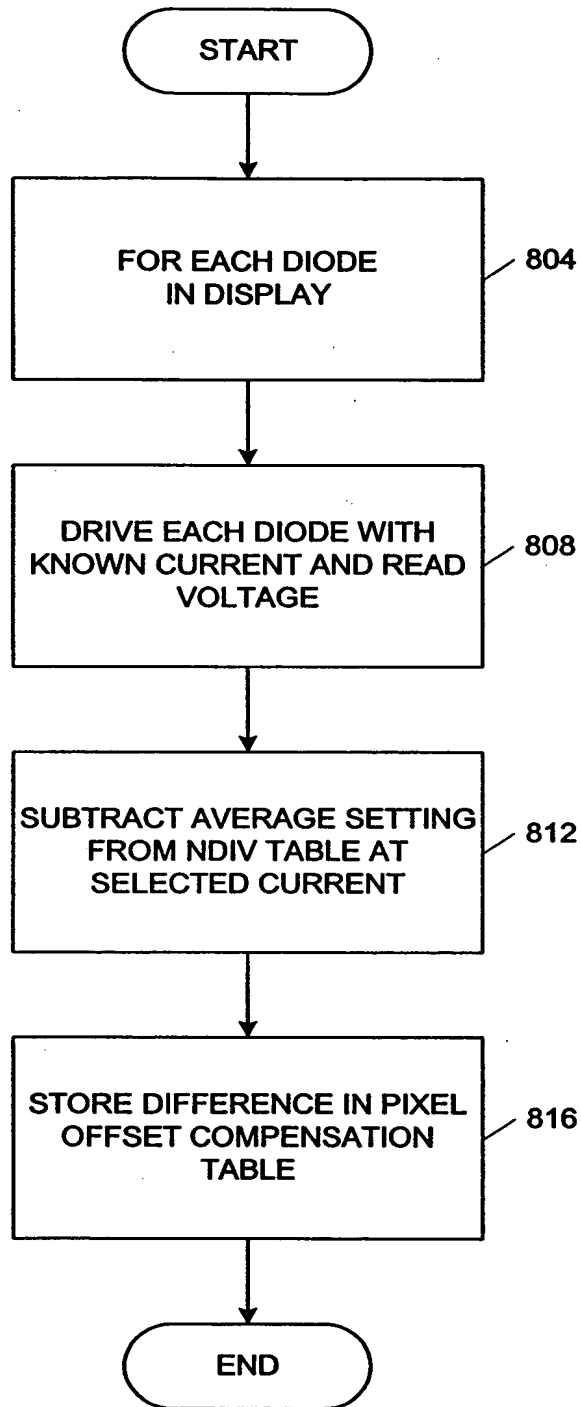


FIG. 8

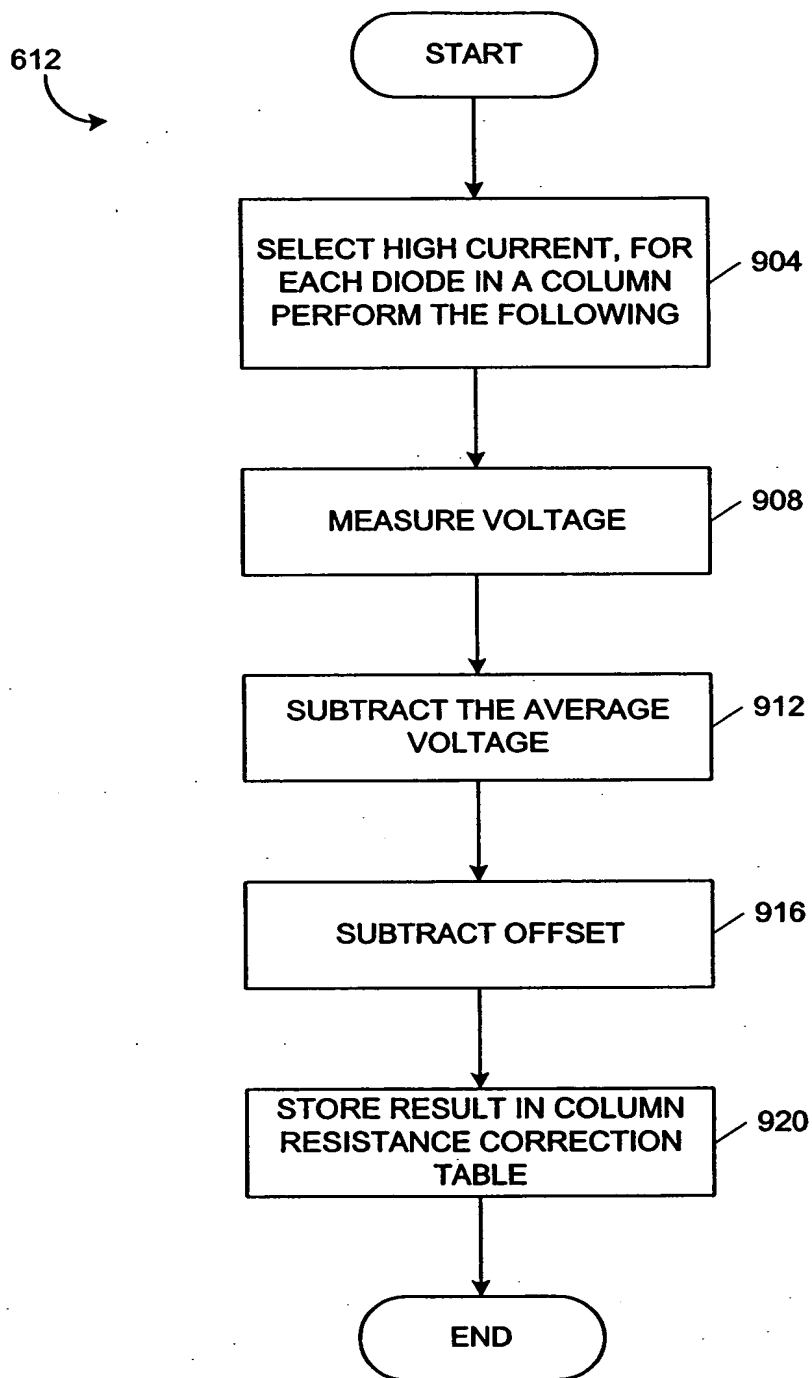


FIG. 9